

AMENDMENTS TO THE SPECIFICATION

This application is related to U.S. patent application serial no. ~~{{attorney docket no. 200208755-1 (P743US)}}~~ 10/644,542, entitled "SYSTEM AND METHOD FOR MEASURING CURRENT;" U.S. patent application serial no. ~~{{attorney docket no. 200208727-1 (P744US)}}~~ 10/644,625, entitled "A SYSTEM FOR AND METHOD OF CONTROLLING A VLSI ENVIRONMENT;" and U.S. ~~patent application serial no. {{attorney docket no. 200208728-1 (P745US)}}~~ Patent No. 6,954,706, entitled "A METHOD FOR MEASURING INTEGRATED CIRCUIT PROCESSOR POWER DEMAND AND ASSOCIATED SYSTEM," filed concurrently herewith, the disclosures of which are hereby incorporated by reference herein in their entirety.